



*Noted*

*#813  
BPC 11403*

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re U.S. Patent Application of )

SATOH et al. )

Application Number: 09/914,429 )

Filed: November 5, 2001 )

For: SEMICONDUCTOR INTEGRATED CIRCUIT HAVING )  
SELF-TESTING CIRCUIT AND METHOD FOR )  
DESIGNING THE SAME (AMENDED) )

Attorney Docket No. HITA.0099 )

Art Unit 2825

Examiner  
Thompson, Annette M

Honorable Assistant Commissioner  
for Patents  
Washington, D.C. 20231

**RESPONSE AND AMENDMENT UNDER 37 C.F.R. §1.111**

Sir:

This is in response to the office action dated May 2, 2003, in the above identified application, the period for response is set to expire on October 2, 2003, with a two-month extension fee filed herewith. Please amend the above identified application with the replacement paragraphs/sections/claims as follows:

RECEIVED  
OCT 10 2003  
TECHNOLOGY CENTER 2800

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